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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10044365	FILING DATE 01/11/2002	CLASS 257	SUBCLASS 30	GAU 2811	EXAMINER Chang, J. D.
<b>**APPLICANTS:</b> Wang Xiaobao; Sung Chiakang; Wang Bonnie; Nguyen Khai;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLN CLAIMS BENEFIT OF 60/315,965 08/29/2001 OC					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> OC					
PG-PUB <input checked="" type="checkbox"/> DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>OC</i>				015114-054910US	
TITLE : On-chip impedance matching circuit					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b> <b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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